

ABSTRACT OF THE DISCLOSURE

Various embodiments of a circuit and method for arbitrating voltage requests in a computer system are disclosed. In a first embodiment, one or more devices in a computer system assert a plurality of voltage requests. These voltage requests are received by an arbiter which outputs a single voltage request. The outputted voltage request is received by one or more power supplies configured to supply a voltage to the devices. In this way, the devices all receive the same voltage even if they originally requested different voltages. In some embodiments, the arbiter may output a voltage request that is selected from the voltage requests asserted by the devices.

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